



Why needs Compression?

- Typical NTSC video
460x340=148000 pels/frame, 30 frames/sec, 16 bits/pel (YIQ)
=71 Mbps
- Bandwidth are limited resource and expensive
- Storage—enormous for uncompressed video
- Compression processing—relative cheap
- Video Camera, VCR, TV Receiver
- Comparison of NTSC and HDTV

Reference:

“Discrete Cosine Transform Algorithms, Advantages, Application”, Rao and Yip, Academic Press, 1990.



Video Compression Applications

- Cable TV distribution
- Interactive communications—videophone, videoconferencing, videotex.
- Digital storage media—CD-ROM, CD-V, digital VTR.
- Network database services video library, video information provider.
- Video-on-demand
- Broadcasting
- Video surveillance



Applications vs. Bit Rates

Application	Standard	Bit Rate Range
Fax	CCITT G3/G4/JBIG	4.8-14.4 kbps
Still Image	JPEG	56/64 kbps
Videophone (analog)		9.6-19.2 kbps
Videophone (digital)	CCITT H.261 px64	64-128 kbps
Videoconferencing	CCITT H.261 px64	320-1920 kbps
Entertainment video	MPEG	1-3 Mbps
Broadcast video	MPEG II	4-10 Mbps
Intraframe JPEG	JPEG	10-20 Mbps
Digital VCR	MPEG II	8-20 Mbps
NTSC (very high quality)	CCIR/ANSI	34/45 Mbps
HDTV (broadcasting)	FCC(US only)	20-25 Mbps
HDTV (studio)		30-45 Mbps



Why can video data be compressed?

Redundancy

- Intraframe
- Interframe



Compression Techniques

- Spatial domain—DPCM, Transform, Subband, Vector quantization
- Temporal domain—frame difference, motion compensation

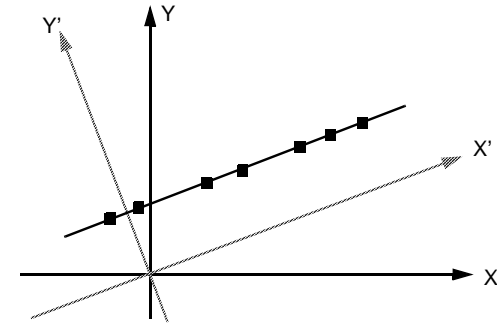
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Transform Coding Concept

- Why can data be compressed?—Redundancy
- Orthogonal transform:
 - decorrelate data into a more compact form so that redundancy can be discarded more easily.



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Orthogonal Transforms

- There are many of them: Which is better?

Karhunen-Loeve Transform (KLT)

optimal, complete decorrelates the random functions in its transform domain.

Discrete Fourier Transform (DFT)

Discrete Slant Transform

Discrete Cosine Transform (DCT) 1974

Discrete Sine Transform (DST)

Discrete Hadamard/Walsh Transform (DHT)

Discrete Harr Transform

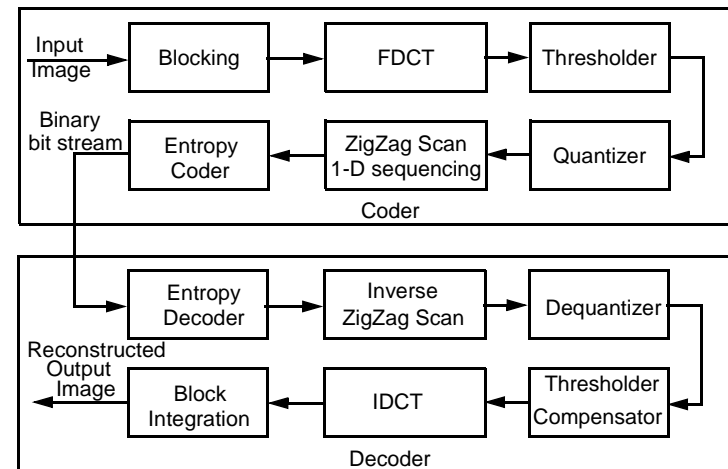
- DCT emerges as the choice of transform for
 - fast algorithms
 - performance (quality)
 - data-independent

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A typical Transform Coder/Decoder



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2D-Discrete Cosine Transform (DCT)

- 8x8 FDCT/IDCT formula

$$F(u, v) = \frac{1}{4} C(u) C(v) \sum_{x=0}^7 \sum_{y=0}^7 f(x, y) \cos \frac{(2x+1)u\pi}{16} \cos \frac{(2y+1)v\pi}{16}$$

$$f(x, y) = \frac{1}{4} \sum_{u=0}^7 \sum_{v=0}^7 C(u) C(v) F(u, v) \cos \frac{(2x+1)u\pi}{16} \cos \frac{(2y+1)v\pi}{16}$$

where $C(k) = \frac{1}{\sqrt{2}}$ if $k = 0$, otherwise $C(k) = 1$

- Separability property

$$F(u, v) = \frac{1}{2} C(u) \sum_{x=0}^7 \left\{ \frac{1}{2} C(v) \sum_{y=0}^7 f(x, y) \cos \frac{(2x+1)u\pi}{16} \right\} \cos \frac{(2y+1)v\pi}{16}$$

The 2-D FDCT can be implemented by 1-D 8-point FDCT along the rows of $f(x, y)$ followed by 1-D 8-point FDCT along the columns of the matrix obtained after the row transformation.



Example of 2D-DCT

f(x,y)								F(u,v)							
f(0,0)	f(1,0)	f(2,0)	f(3,0)	f(4,0)	f(5,0)	f(6,0)	f(7,0)	F(0,0)	F(1,0)	F(2,0)	F(3,0)	F(4,0)	F(5,0)	F(6,0)	F(7,0)
0.125	0.125	0.125	0.125	0.125	0.125	0.125	0.125	1	0	0	0	0	0	0	0
f(0,1)	f(1,1)	f(2,1)	f(3,1)	f(4,1)	f(5,1)	f(6,1)	f(7,1)	F(0,1)	F(1,1)	F(2,1)	F(3,1)	F(4,1)	F(5,1)	F(6,1)	F(7,1)
0.125	0.125	0.125	0.125	0.125	0.125	0.125	0.125	0	0	0	0	0	0	0	0
f(0,2)	f(1,2)	f(2,2)	f(3,2)	f(4,2)	f(5,2)	f(6,2)	f(7,2)	F(0,2)	F(1,2)	F(2,2)	F(3,2)	F(4,2)	F(5,2)	F(6,2)	F(7,2)
0.125	0.125	0.125	0.125	0.125	0.125	0.125	0.125	0	0	0	0	0	0	0	0
f(0,3)	f(1,3)	f(2,3)	f(3,3)	f(4,3)	f(5,3)	f(6,3)	f(7,3)	F(0,3)	F(1,3)	F(2,3)	F(3,3)	F(4,3)	F(5,3)	F(6,3)	F(7,3)
0.125	0.125	0.125	0.125	0.125	0.125	0.125	0.125	0	0	0	0	0	0	0	0
f(0,4)	f(1,4)	f(2,4)	f(3,4)	f(4,4)	f(5,4)	f(6,4)	f(7,4)	F(0,4)	F(1,4)	F(2,4)	F(3,4)	F(4,4)	F(5,4)	F(6,4)	F(7,4)
0.125	0.125	0.125	0.125	0.125	0.125	0.125	0.125	0	0	0	0	0	0	0	0
f(0,5)	f(1,5)	f(2,5)	f(3,5)	f(4,5)	f(5,5)	f(6,5)	f(7,5)	F(0,5)	F(1,5)	F(2,5)	F(3,5)	F(4,5)	F(5,5)	F(6,5)	F(7,5)
0.125	0.125	0.125	0.125	0.125	0.125	0.125	0.125	0	0	0	0	0	0	0	0
f(0,6)	f(1,6)	f(2,6)	f(3,6)	f(4,6)	f(5,6)	f(6,6)	f(7,6)	F(0,6)	F(1,6)	F(2,6)	F(3,6)	F(4,6)	F(5,6)	F(6,6)	F(7,6)
0.125	0.125	0.125	0.125	0.125	0.125	0.125	0.125	0	0	0	0	0	0	0	0
f(0,7)	f(1,7)	f(2,7)	f(3,7)	f(4,7)	f(5,7)	f(6,7)	f(7,7)	F(0,7)	F(1,7)	F(2,7)	F(3,7)	F(4,7)	F(5,7)	F(6,7)	F(7,7)
0.125	0.125	0.125	0.125	0.125	0.125	0.125	0.125	0	0	0	0	0	0	0	0

$$F(0, 0) = \frac{1}{4} C(0) C(0) \sum_{x=0}^7 \sum_{y=0}^7 f(x, y) \cos \frac{(2x+1)0\pi}{16} \cos \frac{(2y+1)0\pi}{16}$$

$$F(0,0) = 1/4 * 1/2 * (64 * 0.125) = 1$$

$$C(0) = 1/\sqrt{2}$$



1D-DCT

- 8-point DCT definition $F(u) = \frac{1}{2} C(u) \sum_{x=0}^7 f(x) \cos \frac{(2x+1)u\pi}{16}$
- Consider \mathbf{f} as a vector of n point data and \mathbf{F} as a vector of transformed values the above transformation can be represent by matrix operations $\mathbf{F} = \mathbf{c}\mathbf{A}\mathbf{f}$
- There are four different 1D-DCTs, the DCT-II was the first to be studied.



Properties of 1D-DCTs

- Only DCT-I and DCT-IV are involutory ($\mathbf{F} = \mathbf{F}^{-1}$)
- Inverse DCT-II \neq DCT-II
- Since Matrix multiplication is a linear operation, all four DCTs retain linearity properties. $\mathbf{F}[\mathbf{ax} + \mathbf{by}] = \mathbf{aF}\mathbf{x} + \mathbf{bF}\mathbf{y}$.
- For properties related to scaling in time, shift in time, differentiation in time, and the convolution, see [DCT9x] by Rao and Yip.



A Fast 1D-DCT Algorithm based on Sparse Matrix Factorization

The 1D-DCT can be represented as $\tilde{x}^{C(2)} = \left(\frac{2}{N}\right)^{1/2} [A_N] \hat{x}$
 Here we show how A_4 can be decomposed into three sparser matrices.

$$C_k^1 = \cos[i\pi/k]$$

$$[A_4] = \begin{bmatrix} 1 & 1 & 1 & 1 \\ \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} \\ C_8^1 & C_8^3 & C_8^5 & C_8^7 \\ C_8^2 & C_8^6 & C_8^4 & C_8^8 \\ C_8^3 & C_8^7 & C_8^1 & C_8^5 \end{bmatrix} \xrightarrow{\text{matrix factorization}} [A_4] = \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 0 & 0 & 1 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 0 \end{bmatrix} \begin{bmatrix} \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} \\ C_4^1 & C_4^3 & C_4^5 & C_4^7 \\ C_8^2 & \mathcal{D}C_8^1 & C_8^4 & \mathcal{D}C_8^3 \\ C_8^1 & C_8^3 & \mathcal{D}C_8^5 & \mathcal{D}C_8^7 \end{bmatrix} \begin{bmatrix} 1 & 0 & 0 & 1 \\ 0 & 1 & 1 & 0 \\ 0 & 1 & \mathcal{D}1 & 0 \\ 1 & 0 & 0 & \mathcal{D}1 \end{bmatrix}$$

$[A_4]x$ requires 16 multiply and 12 add

Here $[A_4]x$ requires only 8 multiply and 8 add



Fast 1D-DCT based on Sparse Matrix Factorization

This matrix operation needs only 2 additions and 2 subtractions.

$$= \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 0 & 0 & 1 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 0 \end{bmatrix} \begin{bmatrix} \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} & 0 & 0 \\ C_4^1 & C_4^3 & 0 & 0 \\ 0 & 0 & \mathcal{D}C_8^1 & C_8^3 \\ 0 & 0 & C_8^3 & C_8^1 \end{bmatrix} \begin{bmatrix} 1 & 0 & 0 & 1 \\ 0 & 1 & 1 & 0 \\ 0 & 1 & \mathcal{D}1 & 0 \\ 1 & 0 & 0 & \mathcal{D}1 \end{bmatrix} \begin{bmatrix} x_1 \\ x_2 \\ x_3 \\ x_4 \end{bmatrix} = \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 0 & 0 & 1 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 0 \end{bmatrix} \begin{bmatrix} \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} & 0 & 0 \\ C_4^1 & C_4^3 & 0 & 0 \\ 0 & 0 & \mathcal{D}C_8^1 & C_8^3 \\ 0 & 0 & C_8^3 & C_8^1 \end{bmatrix} \begin{bmatrix} x_1 + \\ x_2 + \\ x_2 \mathcal{D} \\ x_1 \mathcal{D} \end{bmatrix}$$

This matrix operation needs 8 multiplications, 3 additions, and 1 subtraction.

$$[A_4]x = \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 0 & 0 & 1 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 0 \end{bmatrix} \begin{bmatrix} \frac{1}{\sqrt{2}}(x_1 + x_4) + \frac{1}{\sqrt{2}}(x_2 + x_3) \\ C_4^1(x_1 + x_4) + C_4^3(x_2 + x_3) \\ \mathcal{D}C_8^1(x_2 \mathcal{D}x_3) + C_8^3(x_1 \mathcal{D}x_4) \\ C_8^3(x_2 \mathcal{D}x_3) + C_8^1(x_1 \mathcal{D}x_4) \end{bmatrix} = \begin{bmatrix} \frac{1}{\sqrt{2}}(x_1 + x_4) + \frac{1}{\sqrt{2}}(x_2 + x_3) \\ C_8^3(x_2 \mathcal{D}x_3) + C_8^1(x_1 \mathcal{D}x_4) \\ C_4^1(x_1 + x_4) + C_4^3(x_2 + x_3) \\ \mathcal{D}C_8^1(x_2 \mathcal{D}x_3) + C_8^3(x_1 \mathcal{D}x_4) \end{bmatrix}$$

This matrix operation involves only the shuffling (moving) of data
 In total, there are only 8 multiplications, 5 additions, and 3 subtractions.



Mapping Matrices to Signal Flowgraph

- Matrix operations can be mapped to the signal flowgraph representations.
- Signal flowgraphs can be mapped to the VLSI implementation very easily.
- Signal flowgraphs can be mapped to equivalent software implementations.
- Signal flowgraphs make it easy to capture redundant data movement.

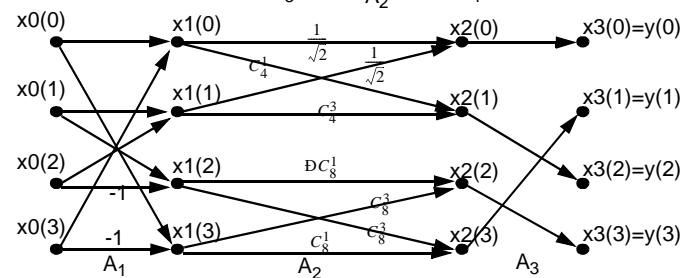
Steps: Map $y = A_N A_2 A_1 x$ to the equivalent signal flowgraph (A_i is a $N \times N$ matrix)

- Draw N nodes top-down on the left hand side of a drawing area, label them $x_0(0)$ to $x_0(N-1)$. These are original values of input vector x .
- To the left of x_0 , draw N nodes and label them $x_1(0)$ to $x_1(N-1)$. x_1 is the result of $A_1 x$.
- Examine values in A_1 . For each of the x_0 elements, say $x_0(i)$, that contributes to $x_1(j)$ draw an arrow from $x_0(i)$ to $x_1(j)$ with its A_1 coefficient as label. E.g., assume that $x_1(i) = A_1(i,1)x_0(1) + A_1(i,3)x_0(3)$ where $A_1(i,1)$ and $A_1(i,3)$ are the only non-negative elements in row $A_1(i)$. Draw an arrow with label $A_1(i,1)$ from $x_0(1)$ to $x_1(i)$. Draw an arrow with label $A_1(i,3)$ from $x_0(3)$ to $x_1(i)$. These arrows represent the "flows" of signal from left to right. Coefficient of 1 is not labelled.
- Repeat Steps 2 and 3 for (A_2, x_2), (A_3, x_3), ..., and (A_N, x_N).
- x_N is the same as y .



Example: Mapping matrices to signal flowgraph

$$[A]x = \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 0 & 0 & 1 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 0 \end{bmatrix} \begin{bmatrix} \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} & 0 & 0 \\ C_4^1 & C_4^3 & 0 & 0 \\ 0 & 0 & \mathcal{D}C_8^1 & C_8^3 \\ 0 & 0 & C_8^3 & C_8^1 \end{bmatrix} \begin{bmatrix} 1 & 0 & 0 & 1 \\ 0 & 1 & 1 & 0 \\ 0 & 1 & \mathcal{D}1 & 0 \\ 1 & 0 & 0 & \mathcal{D}1 \end{bmatrix} \begin{bmatrix} x_1 \\ x_2 \\ x_3 \\ x_4 \end{bmatrix}$$





Algorithm for DCT-II, N=8 and its signal flowgraph

Z. Wang, "Fast algorithms for the discrete W transform and for the discrete Fourier transform," IEEE Trans. Account., Speech, and Signal Process., vol. ASSP-32, pp.8x3-816, Aug. 1984.

$$C_{II}^8 = \frac{1}{\sqrt{2}} \begin{bmatrix} 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 \end{bmatrix}$$

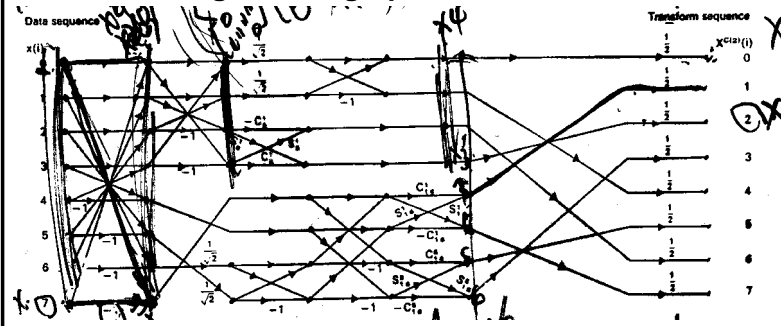
$$x \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 0 & 0 & 1 \\ 0 & 0 & 1 & 0 \\ 0 & 1 & 0 & 0 \end{bmatrix} \begin{bmatrix} \frac{1}{\sqrt{2}} [1 & -1] \\ 0_2 \\ -C_{II}^4 & S_{II}^4 \\ S_{II}^4 & C_{II}^4 \end{bmatrix} \begin{bmatrix} I_2 & I_2 \\ I_2 & -I_2 \end{bmatrix} \begin{matrix} 0_4 \\ 0_4 \end{matrix}$$

$$\times \begin{bmatrix} 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 1 \\ 1 & 0 & 0 & 0 \end{bmatrix} \begin{bmatrix} C_{II}^4 & S_{II}^4 \\ S_{II}^4 & -C_{II}^4 \end{bmatrix} \begin{matrix} 0_2 \\ 0_2 \end{matrix} \begin{bmatrix} I_2 & I_2 \\ I_2 & -I_2 \end{bmatrix} \begin{bmatrix} [I_2] & 0_2 \\ 0_2 & \frac{1}{\sqrt{2}} [1 & -1] \end{bmatrix} \begin{bmatrix} 0 & 0 & 0 & 1 \\ 1 & 0 & 0 & 0 \\ 0 & 0 & 1 & 0 \\ 0 & 1 & 0 & 0 \end{bmatrix}$$

$$\times \begin{bmatrix} I_2 & I_2 \\ I_2 & -I_2 \end{bmatrix} \quad (A.2.3)$$



Signal Flowgraph of DCT-II, N=8



IDCT-II, N=8 and its signal flowgraph

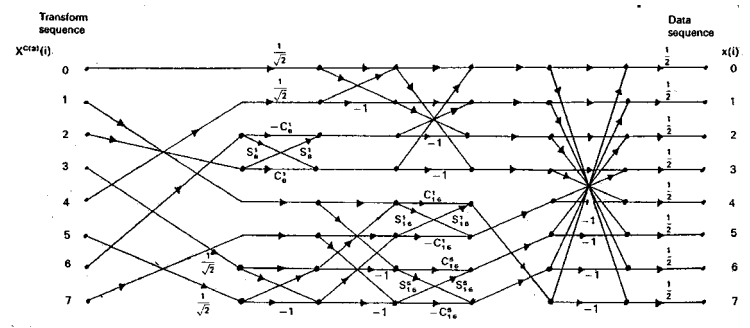
$$C_{II}^8 = \frac{1}{\sqrt{2}} \begin{bmatrix} 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \end{bmatrix}$$

$$\begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 0 & 0 & 1 \\ 0 & 0 & 1 & 0 \\ 0 & 1 & 0 & 0 \end{bmatrix} \begin{bmatrix} \frac{1}{\sqrt{2}} [1 & 1] \\ 0_2 \\ -C_{II}^4 & S_{II}^4 \\ S_{II}^4 & C_{II}^4 \end{bmatrix} \begin{bmatrix} I_2 & I_2 \\ I_2 & -I_2 \end{bmatrix} \begin{matrix} 0_4 \\ 0_4 \end{matrix}$$

$$\times \begin{bmatrix} 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 1 \\ 1 & 0 & 0 & 0 \end{bmatrix} \begin{bmatrix} C_{II}^4 & S_{II}^4 \\ S_{II}^4 & -C_{II}^4 \end{bmatrix} \begin{matrix} 0_2 \\ 0_2 \end{matrix} \begin{bmatrix} I_2 & I_2 \\ I_2 & -I_2 \end{bmatrix} \begin{bmatrix} [I_2] & 0_2 \\ 0_2 & \frac{1}{\sqrt{2}} [1 & -1] \end{bmatrix} \begin{bmatrix} 0 & 0 & 0 & 1 \\ 1 & 0 & 0 & 0 \\ 0 & 0 & 1 & 0 \\ 0 & 1 & 0 & 0 \end{bmatrix}$$



Signal Flowgraph of IDCT-II, N=8





Comparison of Fast DCT-II Algorithms

Algorithm	Complexity	Advantages	Disadvantages
Via N -point FFT [FF-1] (Section 4.2)	$\frac{1}{2}N \log N - N + 2$ complex multiplications	easy to implement using standard FFT routines	slow
Recur. FFT [FF-1, 2, 4] (Section 4.2, 4.6)	$\mu = \frac{1}{2}(N \log N)$ $\alpha = (3 \log N - N + 1)/2$	fast and recursive	complex index mapping
Sparse factors [FRA-1, 2] (Section 4.3)	$\mu = N \log N - 3N/2 + 4$ $\alpha = 3N/2 \log N - 1/2 + 2$	reasonably fast	complex index mapping, nonrecursive
DIT [FRA-8] (Section 4.4)	$\mu = (N \log N)/2 + N/4$ $\alpha = (3N/2 - 1) \log N + N/4 - 1$	fast, recursive decimation in time	moderately complex index mapping
DIF [FRA-9] [FRA-14] (Section 4.4)	$\mu = (N \log N)/2$ $\alpha = (3N/2) \log N - N + 1$	fast, recursive decimation in frequency	moderately complex index mapping
Via WHT [DW-1] (Section 4.5)	dependent on WHT algorithm used	readily implementable, simple index mapping	conversion matrix needed, slow for $N \geq 16$
Via DHT [FRA-16] [FRA-19, 21] (Section 4.5)	same as using FFT	implementable using standard FFT routines	slow
PFA [FRA-10] (Section 4.6)	not applicable	not restricted to radix-2	very complex index mapping
Multiplexed [FRA-13] (Section 4.6)	$\mu = (N - 1)^*$ $\alpha = (N^2 + N - 7/3)^*$	fast, recursive simple index map	slower without multiplexing, shift required
Rotators [DP-22] (Section 4.6)	$(N \log N)/2$ rotations	single processor unit, good structure, simple index map	slow

μ = number of real multiplications, α = number of real additions; log is base 2.

*numbers without multiplexing are $\mu = (N \log N)/2$ and $\alpha = (3N/2) \log N + N^2/2 - N$.

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Exercises

- Develop the computer program which implements the 1D-DCT-II in Page 1x with straight and compile the Fortran program given in the handout which implements Wang's Fast DCT-II and IDCT-II.
- Generate some random data to test and compare the performance of these two DCT-II implementations.
- Compare the original data with the reconstructed data generated by applying Wang's IDCT-II on the transformed data generated by applying Wang's DCT-II on the original data, and discuss the impact of the computation errors introduced by round-off and truncation).
- You can work as a group for this assignment.

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